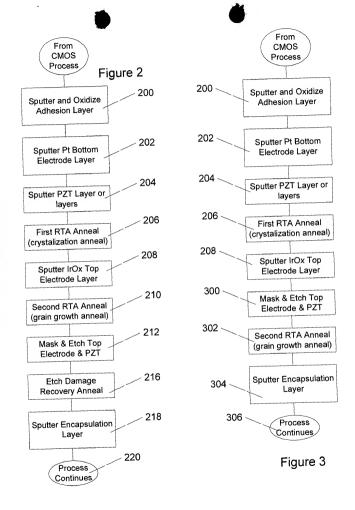


Figure 1



From

CMOS

Figure 4

From